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October 31, 2007

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450


Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 7,257,756
Issued: August 14, 2007
For: **DIGITAL REQUENCY SYNTHESIS CLOCKED
CIRCUITS**
Inventor: Wolff, et al.
Our File No. 42390.P17272

Dear Sir:

Enclosed is the Certificate of Correction for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP


Edwin H. Taylor
Reg. No. 25,129

EHT/sn
Enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,257,756

DATED : August 14, 2007

INVENTOR(S) : Wolff et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 4, at line 51, after "delays," insert --and--.

MAILING ADDRESS OF SENDER
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PATENT NO. 7,257,756

Certificate of Correction (PTO Form 1050)-Amended